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Express Mail # EV 343717525 US

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

OFGS File No. : P/126-223
Inventor : Toru ISHIKAWA
Title : TIMING ADJUSTMENT CIRCUIT AND SEMICONDUCTOR
DEVICE INCLUDING THE SAME
Assignee : Elpida Memory, Inc.

Enclosed herewith please find the following documents in the above-identified application for
United States Letters Patent:

- Print EFS Data Sheet
 19 Pages of Specification including Abstract and Claims
 9 Numbered Claims Calculated as 15 Claims for Fee Purposes
 6 Sheets of Drawing Containing Figures 1 to 6B. (Formal)
 Declaration and Power of Attorney
 Priority is Claimed under 35 U.S.C. §119:
Convention Date 25 October 2002 for Japan Appln. S.N. 311231/2002
 Certified Priority Application
 Assignment
 PT0-1449 and 2 references
 X Return-Addressed Post Card
 ✓ OFGS Check No. 13022, which includes the fee of \$ 770.00, calculated as follows:

Basic Filing Fee: \$ 770.00
Additional Filing Fees:
Total Number of Claims in Excess of 20, times \$18:
Number of Independent Claims in Excess of 3, times \$84:
One or More Multiple Dependent Claims: Total \$280:
Total Filing Fees or
Total Filing Fee Reduced 50% for Small Entity:
Assignment Recording Fee: \$40
TOTAL Filing Fee and Assignment Recording Fee: \$ 770.00

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EXPRESS MAIL CERTIFICATE

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail Post Office to Addressee (mail label #EV 343717525 US) in an envelope addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on October 23, 2003:

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Name of Person Mailing Correspondence

Dorothy Jenkins
Signature

October 29, 2003

Date of Signature

Respectfully submitted,

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INVENTORS DESIGNATION SHEET

TITLE: TIMING ADJUSTMENT CIRCUIT AND SEMICONDUCTOR DEVICE
INCLUDING THE SAME

PRIORITY CLAIMED UNDER 35 U.S.C. 119:

1. COUNTRY: Japan
APPLICATION NO.: 311231/2002
DATE OF FILING: October 25, 2002

INVENTOR #1: Toru ISHIKAWA
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